

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-44. (Canceled)

45. (New) An active matrix substrate, comprising:

a plurality of scan lines;

a plurality of data lines;

a display matrix including a plurality of pixel transistors corresponding to intersections of the plurality of scan lines and the plurality of data lines;

a first data line driving circuit inputting a plurality of analog signals to the plurality of pixel transistors via the plurality of data lines; and

a second data line driving circuit providing a plurality of digital signals to the plurality of data lines.

46. (New) The active matrix substrate according to claim 45, the first data line driving circuit being connected to one end of the plurality of data lines and the second data line driving circuit being connected to the other end of the plurality of data lines.

47. (New) The active matrix substrate according to claim 45, the first data line driving circuit including at least one D/A converter, the at least one D/A converter inputting a plurality of first analog signals to the plurality of pixel transistors via the plurality of data lines.

48. (New) The active matrix substrate according to claim 47, the at least one D/A converter inputting the plurality of analog signals to the plurality of pixel transistors sequentially.

49. (New) The active matrix substrate according to claim 47, the at least one D/A converter inputting the plurality of analog signals to the plurality of pixel transistors simultaneously.

50. (New) The active matrix substrate according to claim 45, further comprising a plurality of first video lines, the first data line driving circuit including a plurality of first switches, each of the plurality of first switches controlling an electrical connection between one of the plurality of first video lines and one of the plurality of data lines.

51. (New) The active matrix substrate according to claim 45, the first data line driving circuit including at least a first group of first switches and a second group of first switches, the first group of first switches being controlled by a first sampling pulse simultaneously, the second group of first switches being controlled by a second sampling pulse simultaneously.

52. (New) The active matrix substrate according to claim 50, each of the first switches of the first group not being adjacent to each other.

53. (New) A display device including the active matrix substrate according to claim 45.

54. (New) An active matrix substrate, comprising:
a plurality of scan lines;
a plurality of data lines;
a display matrix including a plurality of pixel transistors corresponding to intersections of the plurality of scan lines and the plurality of data lines;
a first data line driving circuit providing a plurality of first signals to the plurality of pixel transistors via the plurality of data lines; and

a second data line driving circuit providing a plurality of second signals to the plurality of data lines, the first data line driving circuit having a different function from a function of the second data line driving circuit.

55. (New) The active matrix substrate according to claim 54, the first data line driving circuit being connected to one end of the plurality of data lines and the second data line driving circuit being connected to the other end of the plurality of data lines.

56. (New) The active matrix substrate according to claim 54, the first data line driving circuit including a plurality of first switches, the plurality of first switches connecting the plurality of data lines and a plurality of first video lines, the plurality of first video lines providing the plurality of first signals.

57. (New) The active matrix substrate according to claim 56, the plurality of first switches including a first group of first switches and a second group of first switches, the first group of first switches being controlled by a first sampling pulse simultaneously, the second group of first switches being controlled by a second sampling pulse simultaneously.

58. (New) The active matrix substrate according to claim 57, each of the first switches of the first group not being adjacent to each other.

59. (New) The active matrix substrate according to claim 56, the first data line driving circuit further including at least one first shift resistor that is controlling at least a part of the plurality of first switches.

60. (New) The active matrix substrate according to claim 59, the one first shift resistor inputting a plurality of first pulses to at least one gate circuit, the one gate circuit inputting a plurality of second pulses to the at least a part of the plurality of first switches.

61. (New) The active matrix substrate according to claim 54, the first data line driving circuit including a D/A converter that is connecting the plurality of data lines and providing the plurality of first signals to the plurality of the pixel transistors.

62. (New) The active matrix substrate according to claim 54, the second data line driving circuit including a second shift resistor and a plurality of second switches, the second shift resistor controlling the plurality of the second switches, the plurality of second switches being connected to the plurality of data lines and providing the plurality of second signals to the plurality of pixel transistors.

63. (New) A display device including the active matrix substrate according to claim 54.

64. (New) An active matrix substrate, comprising:

- a plurality of scan lines;
- a plurality of data lines;
- a display matrix including a plurality of pixel transistors corresponding to intersections of the plurality of scan lines and the plurality of data lines;
- a plurality of first switches providing a plurality of first signals to at least more than 2 of the plurality of pixel transistors simultaneously via at least more than 2 of the plurality of data lines; and
- a plurality of first video lines, the plurality of first video lines providing the plurality of first signals to the plurality of first switches,

wherein the plurality of first switches include a first group of first switches and a second group of first switches, the first group of first switches being controlled by a first sampling pulse simultaneously, the second group of the first switches being controlled by a second sampling pulse simultaneously, each of the first switches of the first group not being adjacent to each other.

65. (New) A display device including the active matrix substrate according to claim 64.